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PTO/SB/50 (08-00)

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REISSUE PATENT APPLICATION TRANSMITTAL

Address to:	Attorney Docket No. 93-C-032RE (1678-42)
	First Named Inventor Alex Kalnitsky
Assistant Commissioner for Patents	Original Patent Number 5,986,330
Box Patent Application Washington, DC 20231	Original Patent Issue Date (Month/Day/Year) November 16, 1999
	Express Mail Label No. ET564725085US
APPLICATION FOR REISSUE OF: Utility Patent	
(check applicable box)	y Patent Design Patent Plant Patent
APPLICATION ELEMENTS (37 CFR 1.173)	ACCOMPANYING APPLICATION PARTS
1. * Fee Transmittal Form (e.g., PTO/SB/56)	7. Statement of status/support for all changes to the claims. See
(Submit an original, and a duplicate for fee processing)	37 CFR 1.173(c).
2. Applicant claims small entity status. See 37 CFR 1.27.	8. Original U.S Patent for surrender
3. Specification and Claims in a double column copy of patent format (amended, if appropriate)	Ribboned Original Patent Grant
Drawing(s) (proposed amendments, if appropriate)	Statement of Loss (PTO/SB/55)
5. Reissue Oath / Declaration (original or copy)	9. Foreign Priority Claim (35 U.S.C. 119) (if applicable)
(37 C F R. § 1.175)(PTO/SB/51 or 52) 6. Onginal U.S. Patent currently assigned?	
Yes No	10. Information Disclosure Copies of IDS Statement (IDS)/PTO-1449 Citations
₹7 103 ☐ NO	11. English Translation of Reissue Oath/Declaration
(If Yes, check applicable box(es))	(if applicable)
	12. Preliminary Amendment
Written Consent of all Assignees (PTO/SB/53)	13. Return Receipt Postcard (MPEP 503)
_	(Should be specifically itemized)
37 C F R § 3 73(b) Statement Power of	14. Other: Assent of Assignee; Certificate of Express
(PTO/SB/96) Attorney	<u>Maíl</u>
14. CORRESPONDENCE ADDRESS	
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Original Patent

Patentees: Alex Kalnitsky

Yih-Shung Lin

Patent No.: 5,986,330

Title: ENHANCED PLANARIZATION

TECHNIQUE FOR AN INTEGRATED CIRCUIT

Issued: November 16, 1999

Atty Dk No.: 93-C-032C3 Reissue Application

Applicants: Alex Kalnitsky

Yih-Shung Lin

Serial No.:

Title: ENHANCED PLANARIZATION

TECHNIQUE FOR AN INTEGRATED CIRCUIT

Filing Date: November 16, 2001

Atty Dk No.: 93-C-032RE (1678-42)

CERTIFICATE OF MAILING OR TRANSMISSION

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ASSENT OF ASSIGNEE

TO THE ASSISTANT COMMISSIONER FOR PATENTS:

STMicroelectronics, Inc., assign	ee of U.S. Patent No. 5,986,330, consents to
the filing of reissue application No	(or the present application, if
filed with the initial application papers) for	the reissue of U.S. Patent No. 5,986,330.
	STMicroelectronics, Inc.
	Lisa K. Jorgenson Director of Intellectual Property Assistant Secretary

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Original Patent

Patentees: Alex Kalnitsky

Yih-Shung Lin

Patent No.: 5,986,330

Title: ENHANCED PLANARIZATION

TECHNIQUE FOR AN INTEGRATED CIRCUIT

Issued: November 16, 1999

Atty Dk No.: 93-C-023C3 Reissue Application

Applicants: Alex Kalnitsky

Yih-Shung Lin

Serial No.:

Title: ENHANCED PLANARIZATION

TECHNIQUE FOR AN INTEGRATED CIRCUIT

Filing Date: November 16, 2001

Atty Dk No.: 93-C-023RE (1678-42)

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